Amendments to the Specification:

Please replace the paragraph, beginning at page 47, line 15, with the following rewritten paragraph:

The phase delay phase shift circuit 1001 is comprising a capacitor 1011 as an example of a fifth capacitor in parallel connection and an inductor 1012 as an example of a sixth inductance in series connection. And the phase lead phase shift circuit 1002 is comprising an inductor 1013 1014 as an example of a seventh inductor in parallel connection and a capacitor 1014 1013 as an example of a sixth capacitor in series connection. The filter circuit 1003+ is comprising a capacitor 1015+ and an inductor 1016+ in parallel connection, a capacitor 1017+ in series connection, and a capacitor 1018+ and an inductor 1019+ in parallel connection. The filter circuit 1003- is also comprising likewise. The filter circuits 1003+ and 1003- are band pass filters of passing the signals in the frequency band of the receiving signals. In this case, the signals in the frequency band of the transmitting signals are attenuated, which is not a problem because the transmitting signal leak is originally the jammer to be preferably suppressed.

Respectfully submitted,

Allan Rather, Reg. No. 19,717

Attorney for Applicants

AR/dmw

Dated: May 24, 2004

P.O. Box 980 Valley Forge, PA 19482 (610) 407-0700

The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 24, 2004.

Kathleen

DW_I:\MTS\3490US\SUPPPRELIMAMEMD.DOC